

# MPS6717

## One Watt Amplifier Transistor

### NPN Silicon

#### Features

- Pb-Free Packages are Available\*

#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	$V_{CEO}$	80	Vdc
Collector-Base Voltage	$V_{CBO}$	80	Vdc
Emitter-Base Voltage	$V_{EBO}$	5.0	Vdc
Collector Current - Continuous	$I_C$	500	mAdc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	1.0 8.0	W mW/ $^\circ\text{C}$
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	2.5 20	W mW/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	$T_J, T_{stg}$	-55 to +150	$^\circ\text{C}$

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	125	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	50	$^\circ\text{C}/\text{W}$

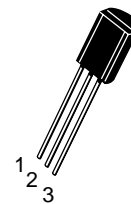
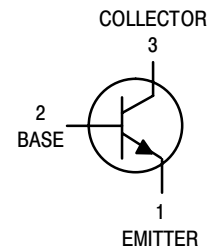
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



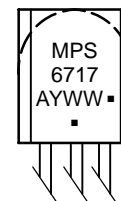
ON Semiconductor®

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TO-92 (TO-226)  
CASE 29-10  
STYLE 1

#### MARKING DIAGRAM



MPS6717 = Device Code  
A = Assembly Location  
Y = Year  
WW = Work Week  
▪ = Pb-Free Package  
(Note: Microdot may be in either location)

#### ORDERING INFORMATION

Device	Package	Shipping†
MPS6717	TO-92	5000 Units / Bulk
MPS6717G	TO-92 (Pb-Free)	5000 Units / Bulk
MPS6717RLRA	TO-92	2000/Tape & Reel
MPS6717RLRAG	TO-92 (Pb-Free)	2000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# MPS6717

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
<b>OFF CHARACTERISTICS</b>				
Collector–Emitter Breakdown Voltage (Note 1) (I <sub>C</sub> = 1.0 mA, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	80	–	Vdc
Collector–Base Breakdown Voltage (I <sub>C</sub> = 100 μA, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	80	–	Vdc
Emitter–Base Breakdown Voltage (I <sub>E</sub> = 10 μA, I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	5.0	–	Vdc
Collector Cutoff Current (V <sub>CB</sub> = 60 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	–	0.1	μA
Emitter Cutoff Current (V <sub>EB</sub> = 5.0 Vdc, I <sub>C</sub> = 0)	I <sub>EBO</sub>	–	10	μA

## ON CHARACTERISTICS

DC Current Gain (I <sub>C</sub> = 50 mA, V <sub>CE</sub> = 1.0 Vdc) (I <sub>C</sub> = 250 mA, V <sub>CE</sub> = 1.0 Vdc)	h <sub>FE</sub>	80 50	– 250	–
Collector–Emitter Saturation Voltage (I <sub>C</sub> = 250 mA, I <sub>B</sub> = 10 mA)	V <sub>CE(sat)</sub>	–	0.5	Vdc
Base–Emitter On Voltage (I <sub>C</sub> = 250 mA, V <sub>CE</sub> = 1.0 Vdc)	V <sub>BE(on)</sub>	–	1.2	Vdc

## SMALL-SIGNAL CHARACTERISTICS

Collector–Base Capacitance (V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>cb</sub>	–	30	pF
Small–Signal Current Gain (I <sub>C</sub> = 200 mA, V <sub>CE</sub> = 5.0 Vdc, f = 20 MHz)	h <sub>fe</sub>	2.5	25	–

1. Pulse Test: Pulse Width ≤ 300 μs; Duty Cycle ≤ 2.0%.

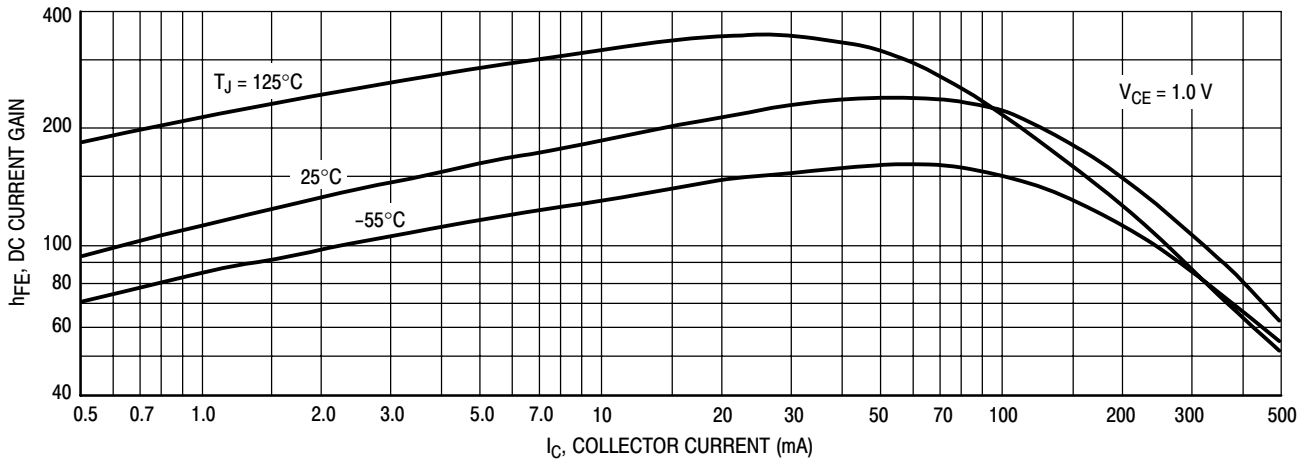


Figure 1. DC Current Gain

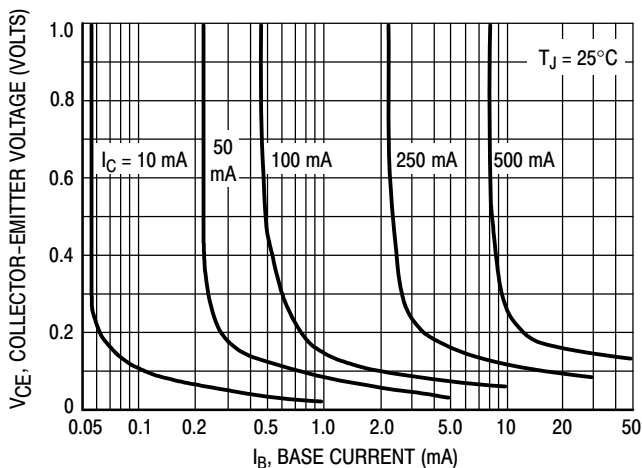


Figure 2. Collector Saturation Region

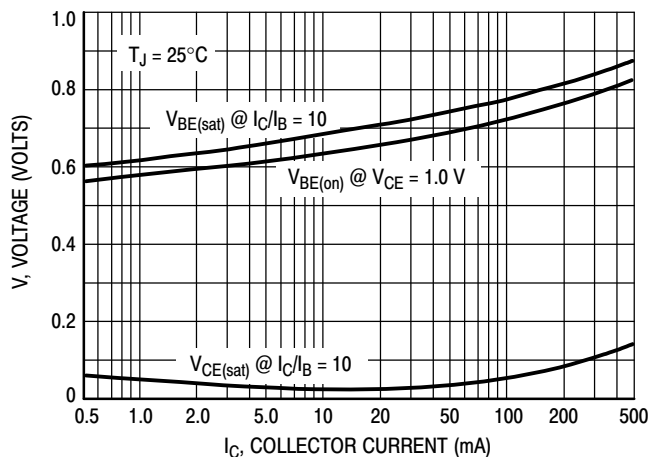


Figure 3. "On" Voltages

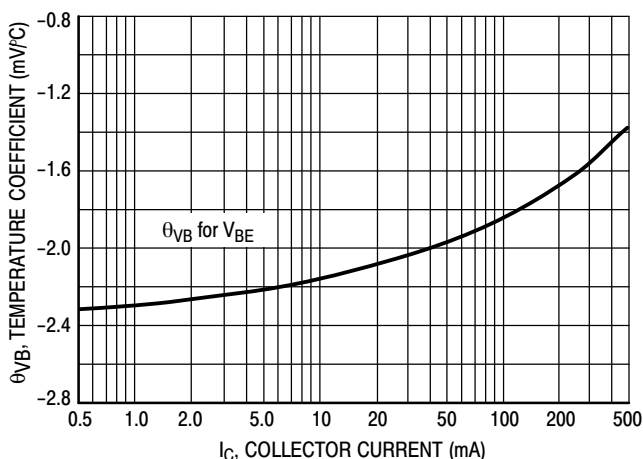


Figure 4. Base-Emitter Temperature Coefficient

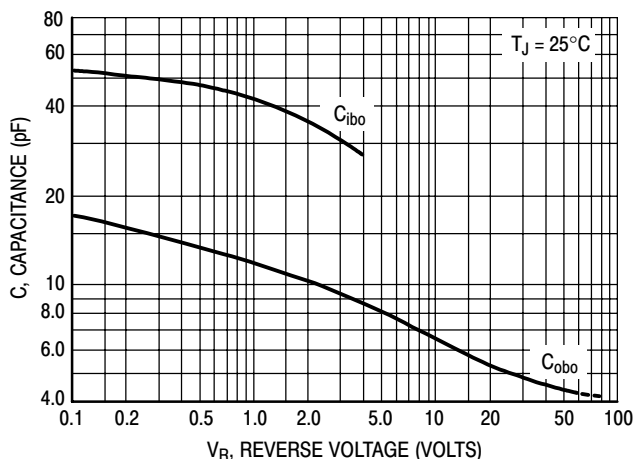


Figure 5. Capacitance

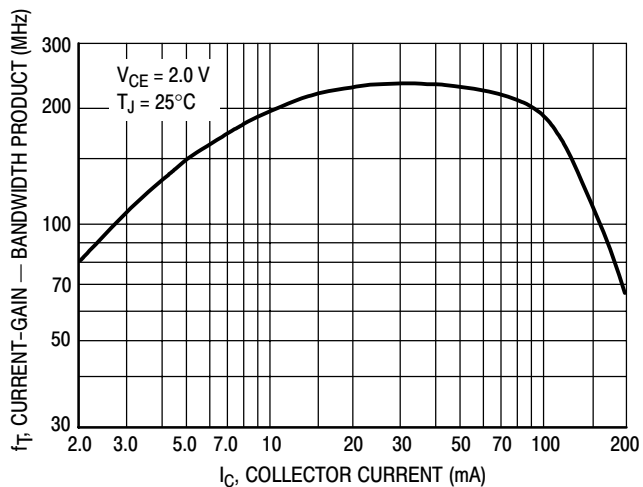


Figure 6. Current-Gain — Bandwidth Product

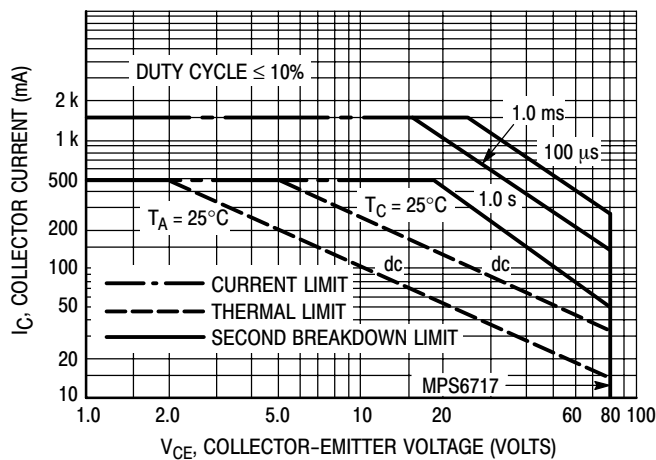
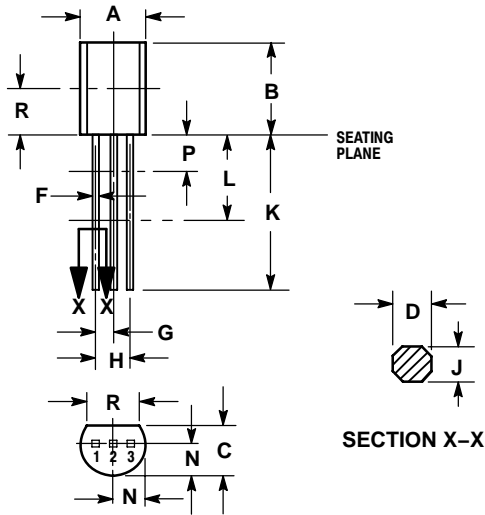


Figure 7. Active Region — Safe Operating Area

# MPS6717

## PACKAGE DIMENSIONS

TO-92 (TO-226)  
CASE 29-10  
ISSUE AL



### NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
4. DIMENSION F APPLIES BETWEEN P AND L. DIMENSIONS D AND J APPLY BETWEEN L AND K MINIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.175	0.205	4.44	5.21
B	0.290	0.310	7.37	7.87
C	0.125	0.165	3.18	4.19
D	0.018	0.021	0.457	0.533
F	0.016	0.019	0.407	0.482
G	0.045	0.055	1.15	1.39
H	0.095	0.105	2.42	2.66
J	0.018	0.024	0.46	0.61
K	0.500	---	12.70	---
L	0.250	---	6.35	---
N	0.080	0.105	2.04	2.66
P	---	0.100	---	2.54
R	0.135	---	3.43	---

### STYLE 1:

- PIN 1. EMITTER
- BASE
- COLLECTOR

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